

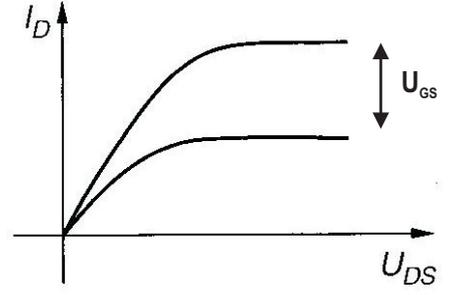
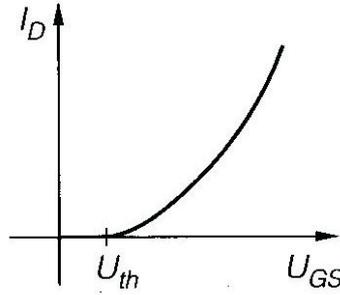
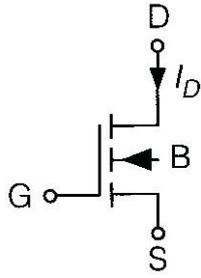
# FET's

## (Übersicht)

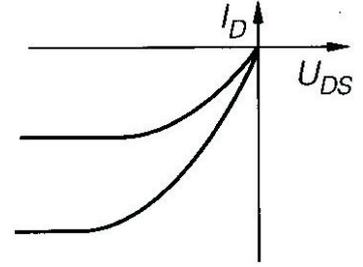
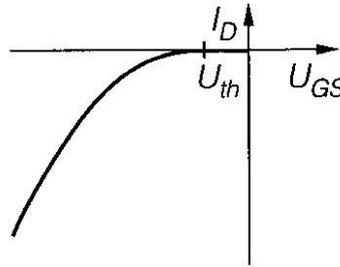
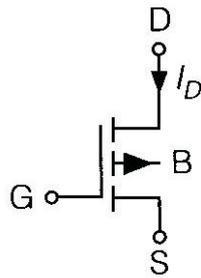
### Transfer-KL

### Ausgangs-KL

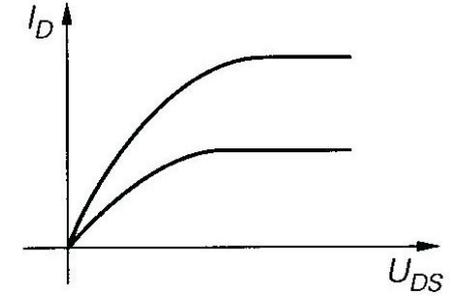
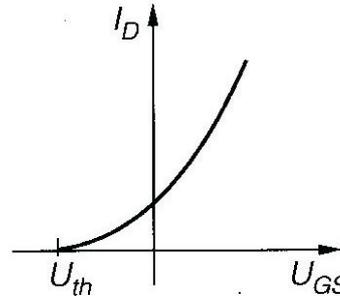
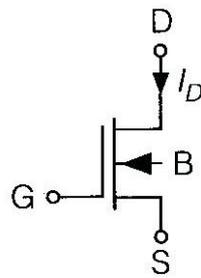
**n-MOSFET**  
selbstsperrend  
(Anreicherung)



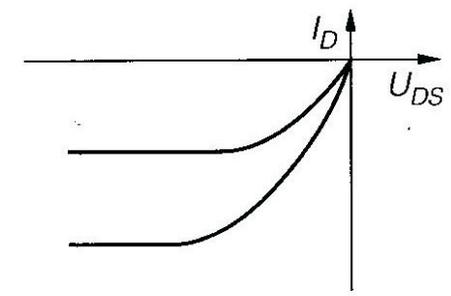
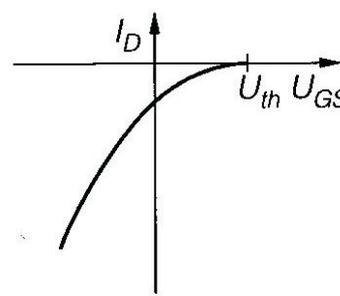
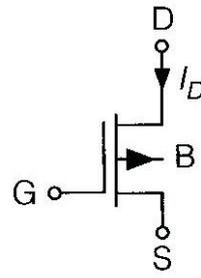
**p-MOSFET**  
selbstsperrend  
(Anreicherung)



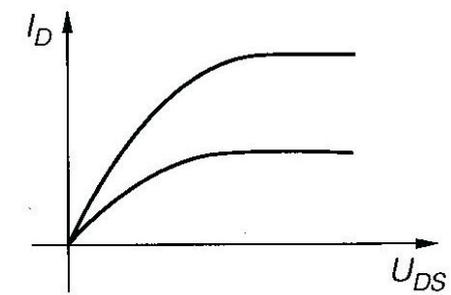
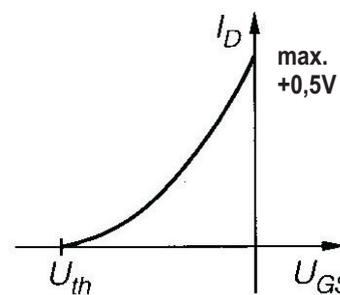
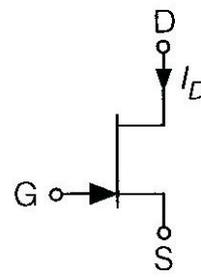
**n-MOSFET**  
selbstleitend  
(Verarmung)



**p-MOSFET**  
selbstleitend  
(Verarmung)



**n-SFET**  
selbstleitend  
(Verarmung)



**p-SFET**  
selbstleitend  
(Verarmung)

